

US Department of Commerce Patent and Trademark Office Form PTO-1449 (Modified)			Atty. Docket No.: 42390P4920C4		Application No.: 10/689,291	
			Applicant: Roussel et al.			
			Filing Date: October 20, 2003			
US Patent Documents						
Examiner's Initials	Date	Document Number	Name	Class	Sub- Class	Filing Date
EC	08/11/98	5,794,060	Hansen et al.	712	1	
EC	08/11/98	5,794,061	Hansen et al.	712	1	
EC	09/01/98	5,802,336	Peleg et al.	712	200	
EC	09/08/98	5,806,049	Petruzzi	705	36	
EC	09/15/98	5,809,321	Hansen et al.	712	1	
EC	10/06/98	5,819,101	Peleg et al.	712	22	
EC	10/13/98	5,822,603	Hansen et al.	712	1	
EC	11/10/98	5,835,392	Dulong et al.	708	404	
EC	03/16/99	5,883,824	Lee et al.	708	445	
EC	03/16/99	5,884,071	Kosaraju	712	245	
EC	09/14/99	5,953,241	Hansen et al.	708	501	
EC	11/09/99	5,983,257	Dulong et al.	708	603	
EC	12/21/99	6,006,318	Hansen et al.	712	28	
EC	03/07/00	6,035,387	Hsu et al.	712	210	
EC	07/10/01	6,260,137	Fleck et al.	712	225	
EC	09/25/01	6,295,599	Hansen et al.	712	32	
EC	04/23/02	6,378,060	Hansen et al.	712	32	
EC	05/07/02	6,385,634	Peleg et al.	708	490	
EC	10/22/02	6,470,370	Fischer et al.	708	622	
EC	06/24/03	6,584,482	Hansen et al.	708	523	
EC	11/04/03	6,643,765	Hansen et al.	712	32	
EC	04/20/04	6,725,356	Hansen et al.	712	210	
Foreign Patent Documents						
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Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)						
<div style="display: flex; justify-content: space-between;"> Examiner <i>Qui</i> Date Considered <i>2/24/05</i> </div>						


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EC		"MIPS Digital Media Extension", pages C1-C40.					
EC		"MIPS Extension for Digital Media with 3D", MIPS Technology, Inc., March 12, 1997, Pages 1-26.					
EC		"64-Bit and Multimedia Extensions in the PA-RISC 2.0 Architecture", Helett Packard, downloaded from Website rblee@cup.hp.com.huck@cup.hp.com. Pages 1-18.					
EC		"The VIS TM Instruction Set", Sun Microsystems, Inc., 1997 pages 1-2.					
EC		"ULTRASPARC TM The Visual Instructions Set (VISTm): On Chip Support for New-Media Processing", Sun Microsystems, Inc., 1996, pages 1-7.					
EC		"ULTRASPARC TM and New Media Support Real-Time MPEG2 Decode with the Visual Instruction Set (VIS TM), Sun Microsystems, Inc., 1996, pages 1-8.					
EC		"ULTRASPARC TM Ultra Port Architecture (UPA): The New Media System Architecture, Sun Microsystems, Inc., 1996, pages 1-4.					
EC		"ULTRASPARC TM Turbocharges Network Operations on New Media Computing, Sun Microsystem, Inc., 1996, pages 1-5.					
EC		"The ULTRASPARC Processor-Technology White Paper, Sun Microsystems, Inc., 1995, 37 pages.					
Examiner <div style="font-family: cursive; font-size: 1.2em;">Eric C.</div>			Date Considered <div style="font-family: cursive; font-size: 1.2em;">2/24/05</div>				

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EC	05/10/94	5,311,508	Buda et al.	370	60	12/23/91
EC	03/27/73	3,723,715	Chen et al.	235	175	08/25/71
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EC		"TM 1000 Preliminary Data Book", Philips Semiconductors, 1997.				
EC		"21164 Alpha TM Microprocessor Data Sheet", Samsung Eleectronics, 1997.				
EC		"Silicon Graphics Introduces Enhanced MIPS ® Architecture to lead the Interactive Digital Revolution, Silicon Graphics", October 21, 1996, downloaded form the Website webmaster @ www.sgi.com, pages 1-2.				
EC		"Silicon Graphics Introduces Compact MIPS ® RISC Microprocessor Code for High Performance at a low Cost", October 21, 1996, downloaded from the Website webmaster @ www.sgi.com, pages 1-2.				
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EC		"MIPS Digital Media Extension", pages C1-C40.				
EC		"MIPS Extension for Digital Media with 3D", MIPS Technologies, Inc., March 12, 1997, pages 1-26.				
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EC	03/21/00	6,041,403	Parker et al.	712	210	09/27/96
EC	01/25/00	6,018,351	Mennemeier et al.	345	437	10/30/97
EC	12/26/89	4,890,218	Bram	364	200	08/10/88
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EC	12/22/98	5,852,726	Lin et al.	395	376	12/19/95
EC	03/02/76	3,941,990	Rabasse	235	176	01/02/74
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EC	08/19/75	3,900,724	McIver et al.	235	164	02/11/74
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EC	03/06/97	WO 97/08608	PCT	G06F	7/00	
EC	06/26/97	WO 97/22923	PCT	G06F	9/34	
EC	06/26/97	WO 97/22924	PCT	G06F	9/34	
EC	06/26/97	WO 97/22921	PCT	G06F	9/00	
Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)						
		gnu@gnu.org (ChangLog) pages A-D.				
EC		Abbott et al., "Broadband Algorithms with the Micro Unity Mediaprocessor", Proceedings of COMPCON 1996, 349-354.				
EC		Hayes et al., "MicroUnity Software Development Environment", Proceedings of COMPCON 1996, 341-348.				
EC		"64-Bit and Multimedia Extensions in the PA-RISC 2.0 Architecture", Helett Packard, downloaded from Website rblee@cup.hp.com.huck@cup.hp.com, pages 1-18.				
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EC	12/01/00	GB 2 339 040 A	Great Britian			
	10/14/99	DE 199 14 617A1	Germany			
EC	10/07/99	WO 99/50740	PCT			
	04/23/87	DE 36 36 106A1	Germany			
EC	07/03/97	WO 97/23821	PCT			
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EC		Santoro, Mark, et al.; SPIM: A Pipelined 64X64 bit Interative Multiplier, IEEE Journal of Solid -State Circuits, Vol. 24, No. 2, April 1989. Pages 487-493.				
EC		Santoro, Mark, et al.; SESSION II: HIGH-SPEED MICROPROCESSOR. WAM 2.6: A Piplelined 64X64b Interative Array Multiplier. 1988 IEEE International Solid State Circuits Conference . Pages 36-37 and 290.				
EC		BIT Preliminary, Bipolar Integrated Technology, Inc. B3110/B3120;B2110/B2120 Floating Point Chip Set. Pages 1-40.				
EC		Eklind, Bob, et al. A SUB 10 nS Bipolar 64 Bit Integrated/Floating Point Processor Implemented On Two Circuits. IEEE 1987 BCTM, pages 101- 104				
EC		Leibowitz, Bob; et al., System Design; ECL gains ground in battle against CMOS. Computer Design; April 1, 1987. Pages 91-95.				
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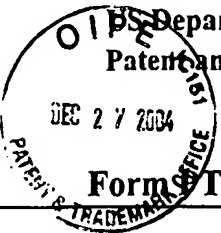
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EC		Farmwald, P. Michael; High Bandwidth Evaluation of Elementary Functions, S-1 Project. IEEE 1981. Pages 139 - 142.				
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EC		Ide, et al.; A 320-MFLOPS CMOS Floating-Point Processing Unit for Superscalar Processors. IEEE Journal of Solid State Circuits, Vol. 28, No. 3, March 1993. Pages 352 - 361.				
EC		Lino, et al.; ISSCC 92 SESSION 6/ MICROPROCESSORS/ TA 6.5: A 289MFLOPS Single-Chip Supercomputer. 1992 IEEE International Solid State Circuits Conference. Pages 112-113.				
EC		Kohn, et al.; Introducing the Intel i860 64 Bit Microprocessor, Intel Corp. August 1989. Pages 15 - 30.				
EC		Lee, Ruby B.; Accelerating Multimedia with Enhanced Microprocessors. Hewlett-Packard. IEEE Micro, April 1995. Pages 22-32.				
EC		Lee, Ruby B.; Realtime MPEG Video via Software Decompression on a PA-RISC Processor. 1995 IEEE, pages 186-192.				
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EC		Spaderna, D., et al.; An Integrated Floating Point Vector Processor for DSP and Scientific Computing". SHARP Microelectronics Technology Inc. IEEE 1989. Pages 8-13.				
EC		Undy, Steve, et al.; A Low-Cost Graphics and Multimedia Workstation Chip Set. April 1994, IEEE micro, pages 10 - 22.				
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EC	06/30/87	4,677,582	Nagafuji	708	518		
EC	07/26/88	4,760,545	Inagami et al.	712	4		
EC	03/07/89	4,811,269	Hirose et al.	708	627		
EC	07/31/90	4,945,479	Rusterholz et al.	712	3		
EC	11/20/90	4,972,362	Elkind et al.	708	632		
EC	01/15/91	4,985,848	Pfeiffer et al.	345	505		
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EC	09/10/91	5,047,975	Patti et al.	708	706		
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EC	12/31/96	5,590,365	Ide et al.	712	218		
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EC		Wang, Mangaser, Shrinivan, "A Processor Architecture for 3D Graphics Calculations, Computer Motion, Inc., pages 1-23.					
EC		Visual Instruction Set (VIS TM), User's Guide, Sun Microsystems, Inc., version 1.1 March 1997.					
EC		"ULTRASPARC TM Turbocharges Network Operations on New Media Computing, Sun Microsystem, Inc., 1996, pages 1-5.					
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